

Birla Vishvakarma Mahavidyalaya (Engineering College)
(An Autonomous Institution)

End Semester Examination Schedule, B. Tech. (PDDC) Regular 1st Semester (First Year)
and Remedial (Backlog) Courses, Academic Year 2016-17

Date, Day	Time	Course Code	Course Title
26.12.2016 Monday	10:30 am to 1:30 pm	CC181	Engineering Mathematics-I
27.12.2016 Tuesday	10:30 am to 1:30 pm	CC182*	Engg. Mathematics - II*
28.12.2016 Wednesday	10:30 am to 1:30 pm	ME181	Computer Programming
29.12.2016 Thursday	10:30 am to 1:30 pm	ME184*	Fluid Mechanics*
30.12.2016 Friday	10:30 am to 1:30 pm	ME182	Engineering Thermodynamics
31.12.2016 Saturday	10:30 am to 1:30 pm	ME185*	Materials Science*
02.1.2017 Monday	10:30 am to 1:30 pm	ME183	Manufacturing Processes
03.1.2017 Tuesday	10:30 am to 1:30 pm	ME186*	Fundamentals of Machine Design*

Note for Students: (i) Mobiles phones are not permitted during examinations.
(ii) Any unfair means will be severely dealt with.
(iii) * indicates remedial exam for backlog students.

Copy to: Head, Mechanical Engg. Department for information please and to display on Faculty and Student Notice Boards. Academic Section for Seating Arrangement preparation and to display on Central Notice Board, Autonomous Exam Notice File.



Controller of Examinations



Dean (Academics)



I/C Principal

Date: 26.11.2016